NSN 5962-01-351-5722

Memory Microcircuit - Page 1 of 2

View Online at https://aerobasegroup.com/nsn/5962-01-351-5722

Overall Height:

0.400 inches

Body Length:

1.280 inches

Body Width:

Between 0.220 inches and 0.310 inches

Body Height:

Between 0.140 inches and 0.185 inches

Maximum Power Dissipation Rating:

1.2 watts

Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

End Application:

Radar set an/sps-4 (v) 7

Features Provided:

Hermetically sealed and burn in and monolithic and electrostatic sensitive and ultraviolet erasable

Inclosure Material:

Ceramic

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Complementary-metal oxide-semiconductor logic

Input Circuit Pattern:

22 input

Case Outline Source And Designator:

D-9 mil-m-38510

Current Rating Per Characteristic:

100.00 milliamperes reverse current, dc absolute

Terminal Surface Treatment:

Solder

Product Name:

And-or-logic array

Voltage Rating And Type Per Characteristic:

-0.5 volts power source and 7.0 volts power source

Capitance Rating Per Characteristic:

10.00 input picofarads and 10.00 output picofarads

Time Rating Per Chacteristic:

25.00 nanoseconds delay

Memory Device Type:

Pal

Test Data Document:

96906-mil-std-883 standard (includes industry or association standards, individual manufacturer standards, etc.).

Terminal Type And Quantity:

24 printed circuit

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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