NSN 5962-01-354-1888

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High performance and high speed and low power

Inclosure Configuration:

Pin grid array

Output Logic Form:

Complementary-metal oxide-semiconductor logic

Design Function And Quantity:

1 adder and 1 multiplier

Terminal Type And Quantity:

132 pin

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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