## NSN 5962-01-357-4211

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**Body Length:** 

Between 0.342 inches and 0.358 inches

**Body Width:** 

Between 0.342 inches and 0.358 inches

**Body Height:** 

Between 0.060 inches and 0.100 inches

**Maximum Power Dissipation Rating:** 

104.5 milliwatts

**Operating Tempurature Range:** 

-55.0/+125.0 degrees celsius

**Storage Tempurature Range:** 

-65.0/+150.0 degrees celsius

**Features Provided:** 

Monolithic and bipolar and cascadable and low power and schottky and w/clear and w/clock

**Inclosure Material:** 

Ceramic

**Inclosure Configuration:** 

Leadless flat pack

**Output Logic Form:** 

Transistor-transistor logic

**Input Circuit Pattern:** 

32 input

**Design Function And Quantity:** 

6 flip-flop, d-type

**Case Outline Source And Designator:** 

C-2 mil-m-38510

**Current Rating Per Characteristic:** 

19.00 milliamperes reverse current, dc absolute

**Terminal Surface Treatment:** 

Solder

**Voltage Rating And Type Per Characteristic:** 

-0.5 volts power source and 7.0 volts power source

**Time Rating Per Chacteristic:** 

20.00 nanoseconds propagation delay time, high to low level output and 18.00 nanoseconds propagation delay time, low to high level output

**Test Data Document:** 

81349-mil-m-38510 specification (includes engineering type bulletins, brochures, etc., that reflect specification type data in specification format; excludes commercial catalogs, industry directories, and similar trade publications, reflecting general type data on certain environmental and performance requirements and test conditions that are shown as "typical", "average", "", etc.).

**Terminal Type And Quantity:** 

20 leadless

Specification Data:

81349-mil-m-38510/372 government specification

Shelf Life:

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Demilitarization:

No

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