# NSN 5962-01-358-4069

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# Body Length:

2.096 inches

### Body Width:

Between 0.510 inches and 0.620 inches

## **Body Height:**

Between 0.155 inches and 0.210 inches

#### **Maximum Power Dissipation Rating:**

750.0 milliwatts

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **Features Provided:**

Hermetically sealed and burn in and ultraviolet erasable and electrostatic sensitive and monolithic

#### Inclosure Material:

Ceramic

### Inclosure Configuration:

Dual-in-line

#### Output Logic Form:

Complementary-metal oxide-semiconductor logic

#### Input Circuit Pattern:

38 input

#### Case Outline Source And Designator:

D-40 mil-m-38510

#### **Current Rating Per Characteristic:**

100.00 milliamperes reverse current, dc absolute

#### **Terminal Surface Treatment:**

Solder

#### **Product Name:**

900 gate erasable programmable logic device

#### Voltage Rating And Type Per Characteristic:

-2.0 volts power source and 7.0 volts power source

#### Capitance Rating Per Characteristic:

20.00 input picofarads and 20.00 output picofarads

#### **Time Rating Per Chacteristic:**

80.00 nanoseconds delay

#### Memory Device Type:

Pal

#### Test Data Document:

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

#### **Terminal Type And Quantity:**

40 printed circuit

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Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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