NSN 5962-01-361-0148

Memory Microcircuit - Page 1 of 1



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Body Length: Between 0.342 inches and 0.358 inches Body Width: Between 0.342 inches and 0.358 inches Body Height: Between 0.060 inches and 0.100 inches Maximum Power Dissipation Rating: 1.0 watts Operating Tempurature Range: -55.0/+125.0 degrees celsius Storage Tempurature Range: -65.0/+150.0 degrees celsius Features Provided: Programmed and high impedance and w/clock and monolithic Inclosure Material: Ceramic Inclosure Configuration: Leadless flat pack
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Inclosure Configuration:
Leadless flat pack
Input Circuit Pattern:
16 input
Case Outline Source And Designator:
C-2 mil-m-38510
Terminal Surface Treatment:
Solder
Product Name:
And-or invert gate array
Voltage Rating And Type Per Characteristic:
-0.5 volts power source and 7.0 volts power source
Memory Device Type:
Pal
Test Data Document:
96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And Quantity:
20 leadless
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli

Fiig:

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