NSN 5962-01-364-3789

Memory Microcircuit - Page 1 of 1



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End Application:	
4920-01-041-5862	
Features Provided:	
Programmed	
Output Logic Form:	
Bipolar metal-oxide semiconductor	
Product Name:	
Pal modification drawing processor address decode	
Memory Device Type:	
Pal	
Test Data Document:	
65552-44-124 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.;	
excludes any specification, standard or other document that may be referenced in a basic governing drawing)	
Terminal Type And Quantity:	
20 printed circuit	
Shelf Life:	
N/a	
Unit Of Measure:	
	
Demilitarization:	
Yes - demil/mli	
Fiig:	
A458a0	