NSN 5962-01-370-9388

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Between 1.270 inches and 1.290 inches

Body Width:

Between 0.290 inches and 0.320 inches

Body Height:

Between 0.180 inches and 0.225 inches

Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

End Application:

Pacer special

Features Provided:

3-state output and schottky and programmable and electrostatic sensitive and tested to mil-std-883 and burn in, mil-std-883, class b and hermetically sealed

Inclosure Material:

Ceramic

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

14 input

Case Outline Source And Designator:

D-9 mil-m-38510

Terminal Surface Treatment:

Solder

Product Name:

Integrated circuit-modified transec microcode

Voltage Rating And Type Per Characteristic:

-0.5 volts total supply and 5.5 volts total supply

Memory Device Type:

Prom

Memory Capacity:

16384 x 2048

Special Features:

Electrostatic discharge sens/hardness critical item; altered item programmed (using 34335/am27s45/bla) using file tpmu17.Hex with checksum be4a. Crit code chg (h) per es lonnell may 01/06

Nuclear Hardness Critical Feature:

Hardened

Test Data Document:

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

Terminal Type And Quantity:

24 printed circuit

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Spec	ifica	ation	Data:
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95105-210-2155-010 manufacturers specification control

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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