# NSN 5962-01-371-2227

Memory Microcircuit - Page 1 of 1



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# Body Length: 1.060 inches Body Width: Between 0.220 inches and 0.310 inches Body Height:

Between 0.130 inches and 0.185 inches

# **Maximum Power Dissipation Rating:**

275.0 milliwatts

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

### **Features Provided:**

Hermetically sealed and programmed and electrostatic sensitive and low power and monolithic and bipolar

#### Inclosure Material:

Ceramic

#### Inclosure Configuration:

Dual-in-line

#### **Output Logic Form:**

Transistor-transistor logic

#### Input Circuit Pattern:

16 input

Case Outline Source And Designator:

D-8 mil-m-38510

#### **Terminal Surface Treatment:**

Solder

#### **Product Name:**

And/or inverted gate array

# Voltage Rating And Type Per Characteristic:

-0.5 volts power source and 7.0 volts power source

#### Memory Device Type:

Pal

# **Test Data Document:**

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

#### **Terminal Type And Quantity:**

20 printed circuit

#### Shelf Life:

N/a

# Unit Of Measure:

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#### Demilitarization:

Yes - demil/mli

# Fiig:

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