## NSN 5962-01-372-1235

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Features Provided:
Monolithic and high speed
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Complementary-metal oxide-semiconductor logic
Design Function And Quantity:
8 buffer, inverting
Terminal Surface Treatment:
Tin
Product Name:
Inverting octal 3-state buffer
Test Data Document:
81349-mil-m-38510 specification (includes engineering type bulletins, brochures, etc., that reflect specification type data in specification
format; excludes commercial catalogs, industry directories, and similar trade publications, reflecting general type data on certain
environmental and performance requirements and test conditions that are shown as "typical", "average", "", etc.).
Terminal Type And Quantity:
20 pin
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli
Fiig:
A458a0
Mil-std (military Standard):
Mil-m-38510 spec.