# NSN 5962-01-372-5545

Memory Microcircuit - Page 1 of 2



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Between 1.270 inches and 1.290 inches

**Body Width:** 

Between 0.590 inches and 0.610 inches

**Body Height:** 

Between 0.205 inches and 0.225 inches

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **End Application:**

Pacer special

# **Features Provided:**

Electrostatic sensitive and programmed and high speed and tested to mil-std-883 and burn in, mil-std-883, class b and hermetically sealed and 3-state output

#### **Inclosure Material:**

Ceramic

#### **Inclosure Configuration:**

Dual-in-line

### **Output Logic Form:**

Transistor-transistor logic

#### **Case Outline Source And Designator:**

D-3 mil-m-38510

### **Terminal Surface Treatment:**

Solder

# **Product Name:**

Microcircuit, digital, memory

# **Voltage Rating And Type Per Characteristic:**

0.0 volts total supply and 7.0 volts total supply

#### **Time Rating Per Chacteristic:**

55.00 nanoseconds access

# **Memory Device Type:**

Prom

### **Memory Capacity:**

32768 x 4096

#### **Special Features:**

Esd; altered item programmed (using 34335/am27s43a/bja or 18234/82hs321a/bja or mmi/53s3281aj/883b) using file mpmu20.Hex with checksum 2170. Crit code chg (h) per es lonnell may 01/06

# **Nuclear Hardness Critical Feature:**

Hardened

# Test Data Document:

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

# **Terminal Type And Quantity:**

24 printed circuit

# NSN 5962-01-372-5545

Memory Microcircuit - Page 2 of 2



|  | Spe | cific | ation | Data |
|--|-----|-------|-------|------|
|--|-----|-------|-------|------|

95105-210-2104-010 manufacturers specification control

Shelf Life:

N/a

**Unit Of Measure:** 

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Demilitarization:

Yes - demil/mli

Fiig:

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