NSN 5962-01-373-5191

Memory Microcircuit - Page 1 of 1



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Features Provided:
Electrostatic sensitive and programmed
nclosure Configuration:
Dual-in-line
nput Circuit Pattern:
12 input
Memory Device Type:
^o al
Test Data Document:
97384-60268-41851 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing,
etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)
Terminal Type And Quantity:
20 printed circuit
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli
Fiig:
A458a0