Memory Microcircuit - Page 1 of 1



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Features Provided:

Electrostatic sensitive

Inclosure Configuration:

Dual-in-line

Input Circuit Pattern:

6 input

Memory Device Type:

Pal

Test Data Document:

97384-60268-41865 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

Terminal Type And Quantity:

20 printed circuit

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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