NSN 5962-01-374-0100

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Padulanath
Body Length:
0.840 inches
Body Width:
Between 0.220 inches and 0.310 inches
Body Height:
0.185 inches
Maximum Power Dissipation Rating: 500.0 milliwatts
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
End Application:
5840-01-229-1276 radar set
Features Provided:
Programmed and 3-state output and w/enable and w/buffered output and monolithic and electrostatic sensitive
Inclosure Material: Ceramic
Inclosure Configuration:
Dual-in-line
Output Logic Form: Transistor-transistor logic
Input Circuit Pattern:
10 input
Case Outline Source And Designator:
D-2 mil-m-38510
Current Rating Per Characteristic:
140.00 milliamperes reverse current, dc absolute
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
-0.5 volts power source and 7.0 volts power source
Time Rating Per Chacteristic:
60.00 nanoseconds access
Memory Device Type:
Prom
Test Data Document:
96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And Quantity:
16 printed circuit
Shelf Life:

N/a

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Unit Of Measure:

Demilitarization:

Yes - demil/mli

Fiig:

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