## NSN 5962-01-377-1128

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## **Body Length:** 1.280 inches **Body Width:** Between 0.220 inches and 0.310 inches **Body Height:** 0.185 inches **Maximum Power Dissipation Rating:** 1.2 watts **Operating Tempurature Range:** -55.0/+125.0 degrees celsius Storage Tempurature Range: -65.0/+150.0 degrees celsius **Features Provided:** High impedance and asynchronous and w/clock and w/reset and bidirectional and bipolar and programmed **Inclosure Material:** Ceramic **Inclosure Configuration:** Dual-in-line **Output Logic Form:** Transistor-transistor logic **Input Circuit Pattern:** 22 input **Case Outline Source And Designator:** D-9 mil-m-38510 **Current Rating Per Characteristic:** 200.00 milliamperes reverse current, dc absolute **Terminal Surface Treatment:** Solder **Product Name:** Registered and-or gate array - invalid memory access detector Voltage Rating And Type Per Characteristic: -0.5 volts power source and 7.0 volts power source **Time Rating Per Chacteristic:** 20.00 nanoseconds propagation delay time, high to low level output and 20.00 nanoseconds propagation delay time, low to high level output Memory Device Type: Pal **Test Data Document:** 96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.). **Terminal Type And Quantity:** 24 printed circuit Shelf Life:

N/a

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Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

A458a0

