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**Inclosure Configuration:**

Dual-in-line

**Output Logic Form:**

Complementary-metal oxide-semiconductor logic

**Input Circuit Pattern:**

3 input

**Design Function And Quantity:**

1 gate, nand

**Terminal Type And Quantity:**

14 pin

**Shelf Life:**

N/a

**Unit Of Measure:**

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**Demilitarization:**

Yes - demil/mli

**Fig:**

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