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Inclosure Configuration:

Flat pack

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

Dual 4 input

Design Function And Quantity:

2 gate, nand

Terminal Type And Quantity:

14 printed circuit

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

No

Fig:

A458a0

Mil-std (military Standard):

Mil-m-38510 spec.