# NSN 5962-01-388-1923

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# **Body Length:**

- 1.198 inches
- **Body Width:**
- 1.198 inches

## **Body Height:**

0.220 inches

## **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

## Storage Tempurature Range:

-65.0/+150.0 degrees celsius

## **End Application:**

B-2

Inclosure Configuration:

Leaded chip carrier

## Output Logic Form:

Complementary-metal oxide-semiconductor logic

## **Criticality Code Justification:**

Feat

## **Design Function And Quantity:**

1 gate, array

# Product Name:

Cmos i/o gate array

## Voltage Rating And Type Per Characteristic:

7.0 volts applied

## **Special Features:**

Hardness critical item; 84 pin leaded chip carrier with custom leads

## **Nuclear Hardness Critical Feature:**

Hardened

## Shelf Life:

N/a

## Unit Of Measure:

```
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```

## Demilitarization:

Yes - demil/mli

Fiig:

A458a0