NSN 5962-01-388-2927

Memory Microcircuit - Page 1 of 1



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| | Maximum | Power | Dissipa | ation | Rating: |
|--|---------|-------|---------|-------|---------|
|--|---------|-------|---------|-------|---------|

1.0 watts

Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

End Application:

Next assembly: 8515198-910 used on (sys): 8515143-910

Features Provided:

Electrostatic sensitive and tested to mil-std-883 and burn in and programmed

Inclosure Configuration:

Leadless flat pack

Output Logic Form:

Complementary-metal oxide-semiconductor logic

Case Outline Source And Designator:

C-2a mil-m-38510

Product Name:

16-input 8-output and-or invert logic array

Voltage Rating And Type Per Characteristic:

-3.0 volts absolute input and 7.0 volts absolute input

Memory Device Type:

Pal

Hybrid Technology Type:

Monolithic

Test Data Document:

81349-mil-m-38510 specification (includes engineering type bulletins, brochures, etc., that reflect specification type data in specification format; excludes commercial catalogs, industry directories, and similar trade publications, reflecting general type data on certain environmental and performance requirements and test conditions that are shown as "typical", "average", "", etc.).

Terminal Type And Quantity:

20 leadless

Specification Data:

67268-5962-88713 government standard

Departure From Cited Designator:

Programmed per: tt8517302-107

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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