NSN 5962-01-389-6021

Memory Microcircuit - Page 1 of 1



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| Maximum Power Dissipation Rating: |
|--|
| 1.0 watts |
| Operating Tempurature Range: |
| -55.0/+125.0 degrees celsius |
| Storage Tempurature Range: |
| -65.0/+150.0 degrees celsius |
| End Application: |
| Instrument landing system 2&3 |
| Features Provided: |
| Monolithic and programmable and ultraviolet erasable |
| Inclosure Configuration: |
| Dual-in-line |
| Output Logic Form: |
| Complementary-metal oxide-semiconductor logic |
| Product Name: |
| Microcircuit, memory, digital, cmos, uv erasable programmable logic device, monolithic silicon |
| Voltage Rating And Type Per Characteristic: |
| -2.0 volts power source and 7.0 volts power source |
| Memory Device Type: |
| Pal |
| Test Data Document: |
| 96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.) |
| Terminal Type And Quantity: |
| 24 printed circuit |
| Shelf Life: |
| N/a |
| Unit Of Measure: |
| |
| Demilitarization: |
| Yes - demil/mli |
| Fiig: |
| A458a0 |
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