NSN 5962-01-392-2821

Memory Microcircuit - Page 1 of 1



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Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Features Provided:
Hermetically sealed and programmed
Inclosure Material:
Ceramic
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
Case Outline Source And Designator:
D-8 mil-m-38510
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
12.0 volts power source
Time Rating Per Chacteristic:
45.00 nanoseconds access
Memory Device Type:
Pal
Test Data Document:
96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And Quantity:
20 printed circuit
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli
Fiig:
A458a0