## NSN 5962-01-393-7848

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Operating Tempurature Range:
-65.0/+175.0 degrees celsius
Features Provided:
Electrostatic sensitive and hermetically sealed
Inclosure Material:
Ceramic
Inclosure Configuration:
Flat pack
Output Logic Form:
Complementary-metal oxide-semiconductor logic
Design Function And Quantity:
4 buffer, noninverting
Case Outline Source And Designator:
F-5 joint electron device engineering council
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
-0.5 volts power source and 18.0 volts power source
Test Data Document:
81349-mil-m-38510 specification (includes engineering type bulletins, brochures, etc., that reflect specification type data in specification
format; excludes commercial catalogs, industry directories, and similar trade publications, reflecting general type data on certain
environmental and performance requirements and test conditions that are shown as "typical", "average", "", etc.).
Terminal Type And Quantity:
16 printed circuit
Specification Data:
81349-mil-m-38510/055 government specification
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli
Fiig:
A458a0