NSN 5962-01-394-8420

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Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Electrostatic sensitive
Inclosure Material:
Ceramic
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Complementary-metal oxide-semiconductor logic
Design Function And Quantity:
2 flip-flop, j-k
Terminal Surface Treatment:
Solder
Product Name:
Dual j-k flip flop w/set and clear
Voltage Rating And Type Per Characteristic:
6.0 volts power source
Terminal Type And Quantity:
16 printed circuit
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli
Fiig:
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