Digital Microcircuit - Page 1 of 1



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Maximum Power Dissipation Rating:

200.0 milliwatts

Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Storage Tempurature Range:

-65.0/+175.0 degrees celsius

Inclosure Material:

Ceramic

Inclosure Configuration:

Flat pack

Output Logic Form:

Complementary-metal oxide-semiconductor logic

Input Circuit Pattern:

Quad 2 input

Design Function And Quantity:

1 gate, nor

Case Outline Source And Designator:

F-2 mil-m-38510

Current Rating Per Characteristic:

10.00 milliamperes reverse current, dc microamperes

Terminal Surface Treatment:

Solder

Product Name:

Digital, cmos, nor gates, positive logic

Voltage Rating And Type Per Characteristic:

-0.5 volts power source and 15.0 volts power source

Special Features:

Positive logic

Test Data Document:

81349-mil-m-38510 specification (includes engineering type bulletins, brochures, etc., that reflect specification type data in specification format; excludes commercial catalogs, industry directories, and similar trade publications, reflecting general type data on certain environmental and performance requirements and test conditions that are shown as "typical", "average", "", etc.).

Terminal Type And Quantity:

14 wire lead

Specification Data:

81349-mil-m-38510/52 government specification

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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