NSN 5962-01-398-3212

Digital Microcircuit - Page 1 of 1



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|---|
| Body Length: |
| Between 1.240 inches and 1.255 inches |
| Body Width: |
| Between 0.250 inches and 0.275 inches |
| Body Height: |
| Between 0.145 inches and 0.165 inches |
| Maximum Power Dissipation Rating: |
| 0.5 watts |
| Operating Tempurature Range: |
| +0.0/+70.0 degrees celsius |
| Storage Tempurature Range: |
| -55.0/+125.0 degrees celsius |
| Inclosure Configuration: |
| Dual-in-line |
| Output Logic Form: |
| Complementary-metal oxide-semiconductor logic |
| Design Function And Quantity: |
| 1 transceiver |
| Current Rating Per Characteristic: |
| 120.00 milliamperes average forward current averaged over a full 60-hz cycle not applicable |
| Product Name: |
| Fast cmos octal latched transceiver |
| Voltage Rating And Type Per Characteristic: |
| -0.5 volts power source and 7.0 volts power source |
| Capitance Rating Per Characteristic: |
| 10.00 input picofarads |
| Special Features: |
| Octal latched |
| Terminal Type And Quantity: |
| 24 printed circuit |
| Shelf Life: |
| N/a |
| Unit Of Measure: |
| |
| Demilitarization: |
| Yes - demil/mli |
| Fiig: |
| A458a0 |