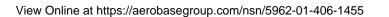
## NSN 5962-01-406-1455

Digital Microcircuit - Page 1 of 1





**Inclosure Material:** Plastic Inclosure Configuration: Leaded chip carrier **Output Logic Form:** Complementary-metal oxide-semiconductor logic **Terminal Surface Treatment:** Solder or tin **Product Name:** Cmos gate array mls acquisition circuit Voltage Rating And Type Per Characteristic: 5.0 volts power source **Terminal Type And Quantity:** 68 j-hook Shelf Life: N/a Unit Of Measure: **Demilitarization:** No Fiig: A458a0

---