NSN 5962-01-412-6862

Memory Microcircuit - Page 1 of 1



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| Operating Tempurature Range: |
|---|
| -55.0/+125.0 degrees celsius |
| Inclosure Material: |
| Ceramic |
| Inclosure Configuration: |
| Pin grid array |
| Terminal Surface Treatment: |
| Solder |
| Product Name: |
| Cmos gate array |
| Voltage Rating And Type Per Characteristic: |
| 7.0 volts power source |
| Memory Device Type: |
| Pal |
| Terminal Type And Quantity: |
| 145 pin |
| Shelf Life: |
| N/a |
| Unit Of Measure: |
| |
| Demilitarization: |
| No |
| Fiig: |
| A458a0 |
| |
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