NSN 5962-01-414-3885

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Inclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
Design Function And Quantity:
3 gate, and-or invert
Special Features:
29 aug 2000; item reviewed under girder project. Non-concur to cancelation request; commercial reliability
Terminal Type And Quantity:
14 pin
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
No
Fiig:
A458a0