NSN 5962-01-418-5265

Linear Microcircuit - Page 1 of 1



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Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
End Application:
Pacer dawn
Features Provided:
Electrostatic sensitive
Inclosure Material:
Ceramic
Inclosure Configuration:
Dual-in-line
Input Circuit Pattern:
Dual 4 input
Criticality Code Justification:
Cbbl
Design Function And Quantity:
2 timer
Terminal Surface Treatment:
Solder
Product Name:
Monolythic silicon linear cmos precision timer
Voltage Rating And Type Per Characteristic:
5.0 volts power source and 18.0 volts power source
Terminal Type And Quantity:
14 printed circuit
Specification Data:
67268-5962-89503 government specification
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli
Fiig:
A458a0