## NSN 5962-01-419-8149

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| Maximum Power Dissipation Rating:  |
|--|
| 104.5 milliwatts   |
| Operating Tempurature Range:   |
| -55.0/+125.0 degrees celsius   |
| Storage Tempurature Range:   |
| -65.0/+150.0 degrees celsius   |
| Features Provided:   |
| Electrostatic sensitive  |
| Inclosure Configuration:   |
| Flat pack  |
| Output Logic Form:   |
| Transistor-transistor logic  |
| Design Function And Quantity:  |
| 1 flip-flop, j-k   |
| Terminal Surface Treatment:  |
| Solder   |
| Voltage Rating And Type Per Characteristic:  |
| 0.5 volts multiplex input signal and 7.0 volts multiplex input signal  |
| Test Data Document:  |
| $96906 \hbox{-mil-std-}883 \ standard \ (includes \ industry \ or \ association \ standards, \ individual \ manufactureer \ standards, \ etc.).$ |
| Terminal Type And Quantity:  |
| 14 flat leads  |
| Shelf Life:  |
| N/a  |
| Unit Of Measure:   |
|  |
| Demilitarization:  |
| Yes - demil/mli  |
| Fiig:  |
| A458a0   |
|  |
|  |