## NSN 5962-01-431-7373

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| Operating Tempurature Range:  |
|-------------------------------|
| -25.0/+125.0 degrees celsius  |
| Features Provided:            |
| Electrostatic sensitive       |
| Inclosure Configuration:      |
| Dual-in-line                  |
| Output Logic Form:            |
| Transistor-transistor logic   |
| Design Function And Quantity: |
| 1 adder, full                 |
| Terminal Type And Quantity:   |
| 16 pin                        |
| Shelf Life:                   |
| N/a                           |
| Unit Of Measure:              |
|                               |
| Demilitarization:             |
| No                            |
| Fiig:                         |
| A458a0                        |
|                               |