NSN 5962-01-431-9181

Memory Microcircuit - Page 1 of 1



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Maximum Power Dissipation Rating:

2.0 watts

Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

End Application:

Pacer dawn

Features Provided:

Electrostatic sensitive and tested to mil-std-883 and programmed and burn in, mil-std-883, class b and erasable

Inclosure Material:

Silicon

Inclosure Configuration:

Pin grid array

Output Logic Form:

Complementary-metal oxide-semiconductor logic

Criticality Code Justification:

Cbbl

Product Name:

Mckt, digital, uv erasable; cmos; programmed logic device

Voltage Rating And Type Per Characteristic:

4.5 volts power source and 5.5 volts power source

Memory Device Type:

Pal

Special Features:

Altered item programmed using 67268/5962-8946901yx using firmware version/description document fdd/vdd 0249-01.02

Test Data Document:

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

Terminal Type And Quantity:

68 pin

Shelf Life:

N/a

Unit Of Measure:

Demilitarization:

No

Fiig:

A458a0