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Operating Temperature Range:

-55.0/+125.0 degrees celsius

Inclosure Configuration:

Flat pack

Output Logic Form:

Transistor-transistor logic

Design Function And Quantity:

1 latch

Product Name:

Latch, jtag, 18-bit

Voltage Rating And Type Per Characteristic:

7.0 volts power source

Nuclear Hardness Critical Feature:

Hardened

Terminal Type And Quantity:

56 flat leads

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

No

Fig:

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