## NSN 5962-01-435-1324

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Maximum Power Dissipation Rating:
500.0 milliwatts
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Electrostatic sensitive
Inclosure Configuration:
Flat pack
Output Logic Form:
Transistor-transistor logic
Design Function And Quantity:
1 flip-flop, j-k
Product Name:
Dual negative edge triggered jk, flip-flop with asynchronous set and clear, ttl compatible inputs
Voltage Rating And Type Per Characteristic:
-0.5 volts power source and 6.0 volts power source
Test Data Document:
96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And Quantity:
16 wire lead
Shelf Life:
N/a
Unit Of Measure:
<del></del>
Demilitarization:
Yes - demil/mli
Fiig:
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