NSN 5962-01-436-2476

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Maximum Power Dissipation Rating:
500.0 milliwatts
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Monolithic
Inclosure Configuration:
Leaded chip carrier
Output Logic Form:
Complementary-metal oxide-semiconductor logic
Design Function And Quantity:
2 flip-flop, j-k
Terminal Surface Treatment:
Solder
Product Name:
Microcircuits, digital, advanced cmos, dual j-k flip-flop with set and clear, ttl compatible inputs, monolithic silicon
Voltage Rating And Type Per Characteristic:
6.0 volts power source
Special Features:
Lead chip carrier with 20 s shaped terminals
Test Data Document:
96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And Quantity:
20 j-hook
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
No
Fiig:
A458a0