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Operating Temperature Range:

-55.0/+125.0 degrees celsius

Storage Temperature Range:

-65.0/+150.0 degrees celsius

End Application:

Pacer dawn

Features Provided:

Electrostatic sensitive

Inclosure Material:

Ceramic

Inclosure Configuration:

Leaded chip carrier

Output Logic Form:

Transistor-transistor logic

Criticality Code Justification:

Cbbl

Design Function And Quantity:

1 shift register

Terminal Surface Treatment:

Solder

Product Name:

8-bit parallel-in shift register with clear..Shottky ttl

Voltage Rating And Type Per Characteristic:

4.5 volts power source and 5.5 volts power source

Terminal Type And Quantity:

20 leadless

Specification Data:

81349--38510 government specification

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

No

Fig:

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