NSN 5962-01-439-0365

Digital Microcircuit - Page 1 of 1



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Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
End Application:
Pacer dawn
Features Provided:
Electrostatic sensitive
Inclosure Material:
Ceramic
Inclosure Configuration:
Leaded chip carrier
Output Logic Form:
Transistor-transistor logic
Criticality Code Justification:
Cbbl
Design Function And Quantity:
1 shift register
Terminal Surface Treatment:
Solder
Product Name:
8-bit parallel-in shift register with clearShottky ttl
Voltage Rating And Type Per Characteristic:
4.5 volts power source and 5.5 volts power source
Terminal Type And Quantity:
20 leadless
Specification Data:
8134938510 government specification
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
No
Fiig:
A458a0