NSN 5962-01-449-6836

Memory Microcircuit - Page 1 of 1



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Body Length:

Between 0.800 inches and 0.840 inches

Body Width:

Between 0.392 inches and 0.408 inches

Body Height:

0.900 inches

Maximum Power Dissipation Rating:

1.0 watts

Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

Features Provided:

Monolithic and electrostatic sensitive and low power

Inclosure Material:

Ceramic

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Complementary-metal oxide-semiconductor logic

Terminal Surface Treatment:

Solder

Product Name:

Microcircuit, memory, digital, cmos, 128k x 8 static random access memory (sram) low power, monolithic silicon

Voltage Rating And Type Per Characteristic:

-0.5 volts input and 7.0 volts input

Time Rating Per Chacteristic:

70.00 nanoseconds access

Memory Device Type:

Ram

Test Data Document:

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

Terminal Type And Quantity:

32 printed circuit

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

No

Fiig:

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