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Operating Temperature Range:

-55.0/+125.0 degrees celsius

End Application:

Fy92/93 ang tk e/i fscm 04713

Features Provided:

Electrostatic sensitive

Inclosure Material:

Plastic

Inclosure Configuration:

Leaded chip carrier

Output Logic Form:

Complementary-metal oxide-semiconductor logic

Design Function And Quantity:

2 flip-flop, j-k, clocked

Case Outline Source And Designator:

751b-05 joint electron device engineering council

Product Name:

Dual j-k filp-flop with set and reset

Voltage Rating And Type Per Characteristic:

2.0 volts applied and 6.0 volts applied

Terminal Type And Quantity:

16 gullwing

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

No

Fiig:

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