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Operating Temperature Range:

-40.0/+85.0 degrees celsius

End Application:

Fy92/93 ang tk e/i fscm 24226

Features Provided:

Electrostatic sensitive

Inclosure Material:

Plastic

Inclosure Configuration:

Leaded chip carrier

Design Function And Quantity:

1 phase locked loop

Case Outline Source And Designator:

Ms-012-ac joint electron device engineering council

Product Name:

Cmos phase lock loop

Voltage Rating And Type Per Characteristic:

-0.5 volts applied and 7.0 volts applied

Terminal Type And Quantity:

16 gullwing

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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