NSN 5962-01-461-6629

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Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
End Application:
Joint strs rd e/i fscm 95542
Features Provided:
Burn in and cascadable and w/clock and electrostatic sensitive
Inclosure Configuration:
Flat pack
Output Logic Form:
Transistor-transistor logic and complementary-metal oxide-semiconductor logic
Design Function And Quantity:
1 gate, array
Terminal Surface Treatment:
Solder
Product Name:
Microcircuit, cmos, gate array
Voltage Rating And Type Per Characteristic:
-0.3 volts power source and 7.0 volts power source
Test Data Document:
96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And Quantity:
132 pin
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
No
Fiig:
A458a0