NSN 5962-01-462-1577

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Maximum Power Dissipation Rating:
500.0 milliwatts
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
End Application:
A/f-18-dcn e/i fscm 93322
Features Provided:
W/clock and monolithic
Inclosure Configuration:
Leaded chip carrier
Output Logic Form:
Complementary-metal oxide-semiconductor logic and transistor-transistor logic
Design Function And Quantity:
1 buffer
Terminal Surface Treatment:
Solder
Product Name:
Ceramic chip carrier assembly, flat j-leaded
Voltage Rating And Type Per Characteristic:
-0.5 volts power source and 7.0 volts power source
Special Features:
W/non-inverting 3-state outputs and esd
Test Data Document:
96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And Quantity:
20 j-hook
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
No
Fiig:
A458a0