NSN 5962-01-466-0762

Digital Microcircuit - Page 1 of 1



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Maximum Power Dissipation Rating:
500.0 milliwatts
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
End Application:
F15 acft
Features Provided:
Monolithic and dual positive edge triggered
Inclosure Configuration:
Flat pack
Output Logic Form:
Complementary-metal oxide-semiconductor logic
Design Function And Quantity:
2 flip-flop, j-k
Terminal Surface Treatment:
Solder
Product Name:
Flip-flip, jk, dual, positive edge-triggered, w/cle & set
Voltage Rating And Type Per Characteristic:
-0.5 volts input and 6.0 volts input
Test Data Document:
$96906 \hbox{-mil-std-}883 \ standard \ (includes \ industry \ or \ association \ standards, \ individual \ manufactureer \ standards, \ etc.).$
Terminal Type And Quantity:
16 flat leads
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
No
Fiig:
A458a0