NSN 5962-01-475-0027

Digital Microcircuit - Page 1 of 1



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Body Length:

0.785 inches

Body Width:

Between 0.220 inches and 0.310 inches

Body Height:

0.200 inches

Maximum Power Dissipation Rating:

700.0 milliwatts

Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

End Application:

1430-01-446-5321

Features Provided:

Negative edge triggered and low power

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Complementary-metal oxide-semiconductor logic

Design Function And Quantity:

2 flip-flop, j-k

Terminal Surface Treatment:

Solder

Product Name:

Microcircuit, digital, dual j-k flip-flop

Voltage Rating And Type Per Characteristic:

3.0 volts input and 15.0 volts input

Special Features:

N- and p-channel enhancement mode transistors

Test Data Document:

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

Terminal Type And Quantity:

14 printed circuit

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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