NSN 5962-01-476-1997

Digital Microcircuit - Page 1 of 1



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| Body Length: |
|---|
| 0.785 millimeters |
| Body Width: |
| Between 0.290 millimeters and 0.320 millimeters |
| Body Height: |
| 0.200 millimeters |
| Maximum Power Dissipation Rating: |
| 7.00 milliwatts |
| Operating Tempurature Range: |
| -55.0/+125.0 degrees celsius |
| Storage Tempurature Range: |
| -65.0/+150.0 degrees celsius |
| End Application: |
| Apx-100 airborne radar |
| Features Provided: |
| Electrostatic sensitive and low power |
| Inclosure Material: |
| Ceramic |
| Inclosure Configuration: |
| Dual-in-line Dual-in-line |
| Output Logic Form: |
| Complementary-metal oxide-semiconductor logic |
| Design Function And Quantity: |
| 1 gate, nor |
| Terminal Surface Treatment: |
| Solder |
| Product Name: |
| Microcircuit, digital cmos quad 2-input nor gates |
| Voltage Rating And Type Per Characteristic: |
| 18.0 volts input |
| Test Data Document: |
| 96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.) |
| Terminal Type And Quantity: |
| 14 printed circuit |
| Shelf Life: |
| N/a |
| Unit Of Measure: |
| |
| Demilitarization: |
| Yes - demil/mli |
| Fiig: |
| A458a0 |