## NSN 5962-01-489-0725

Digital Microcircuit - Page 1 of 1



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| Maximum Power Dissipation Rating:  |
|--|
| 805.0 milliwatts   |
| Operating Tempurature Range:   |
| -55.0/+125.0 degrees celsius   |
| Storage Tempurature Range:   |
| -65.0/+150.0 degrees celsius   |
| End Application:   |
| Joint stars target radar system  |
| Features Provided:   |
| Monolithic   |
| Inclosure Configuration:   |
| Flat pack  |
| Output Logic Form:   |
| Emitter-coupled logic  |
| Design Function And Quantity:  |
| 3 flip-flop, d-type  |
| Terminal Surface Treatment:  |
| Solder   |
| Product Name:  |
| Microcircuit, digital, ecl, triple d flip-flop, monolithic silicon   |
| Voltage Rating And Type Per Characteristic:  |
| -7.0 volts power source and 0.5 volts power source   |
| Test Data Document:  |
| $96906 \hbox{-mil-std-}883 \ standard \ (includes \ industry \ or \ association \ standards, \ individual \ manufactureer \ standards, \ etc.).$ |
| Terminal Type And Quantity:  |
| 24 flat leads  |
| Shelf Life:  |
| N/a  |
| Unit Of Measure:   |
|  |
| Demilitarization:  |
| Yes - demil/mli  |
| Fiig:  |
| A458a0   |
|  |