# NSN 5962-01-490-0043

Memory Microcircuit - Page 1 of 1



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# Features Provided: Electrostatic sensitive Inclosure Material: Ceramic Inclosure Configuration: Dual-in-line Output Logic Form: Complementary-metal oxide-semiconductor logic and transistor-transistor logic Input Circuit Pattern:

2 input and 3 input and 9 input

## Criticality Code Justification:

Cbbl

**Terminal Surface Treatment:** 

Solder

**Product Name:** 

Microcircuit, programmed

#### Voltage Rating And Type Per Characteristic:

5.0 volts power source

Memory Device Type:

Eprom

#### **Terminal Type And Quantity:**

28 printed circuit

### Shelf Life:

N/a

#### Unit Of Measure:

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#### Demilitarization:

Yes - demil/mli

Fiig:

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