## NSN 5962-01-494-3565

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## **Body Length:** 0.785 inches **Body Width:** Between 0.220 inches and 0.310 inches **Body Height:** 0.200 inches **Operating Tempurature Range:** -55.0/+125.0 degrees celsius Storage Tempurature Range: -65.0/+150.0 degrees celsius **End Application:** An/ajn-18 indicator set/f-15 **Features Provided:** Electrostatic sensitive and low power and hermetically sealed **Inclosure Configuration:** Dual-in-line **Output Logic Form:** Transistor-transistor logic **Design Function And Quantity:** 1 flip-flop, j-k **Terminal Surface Treatment:** Solder **Product Name:** Integrated circuits/flip-flops, j-k type Voltage Rating And Type Per Characteristic: 8.0 volts total supply **Test Data Document:** 96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.). **Terminal Type And Quantity:** 14 printed circuit Shelf Life: N/a Unit Of Measure: --**Demilitarization:** Yes - demil/mli Fiig: A458a0