## NSN 5962-01-495-0393

Digital Microcircuit - Page 1 of 1



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| Operating Tempurature Range:   |
|--|
| -55.0/+125.0 degrees celsius   |
| Storage Tempurature Range:   |
| -65.0/+150.0 degrees celsius   |
| End Application:   |
| Kg-75  |
| Features Provided:   |
| Electrostatic sensitive and 3-state output and high impedance  |
| Inclosure Configuration:   |
| Flat pack  |
| Output Logic Form:   |
| Transistor-transistor logic  |
| Design Function And Quantity:  |
| 1 flip-flop, edge triggered, d-type  |
| Terminal Surface Treatment:  |
| Solder   |
| Product Name:  |
| 10-bit bus-interface flip-flops with 3-state outputs   |
| Voltage Rating And Type Per Characteristic:  |
| -0.5 volts input and 7.0 volts input   |
| Test Data Document:  |
| 96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.). |
| Terminal Type And Quantity:  |
| 24 printed circuit   |
| Shelf Life:  |
| N/a  |
| Unit Of Measure:   |
|  |
| Demilitarization:  |
| Yes - demil/mli  |
| Fiig:  |
| A458a0   |
|  |