# NSN 5962-01-495-7987

Digital Microcircuit - Page 1 of 1



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## **Body Length:**

Between 0.740 inches and 0.780 inches

## **Body Width:**

0.300 inches

## **Body Height:**

0.130 inches

## **Maximum Power Dissipation Rating:**

600.0 milliwatts

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

## Storage Tempurature Range:

-65.0/+150.0 degrees celsius

## End Application:

ELEC-TT-EST-EQUI

#### **Features Provided:**

## Electrostatic sensitive

Inclosure Material:

## Plastic

Inclosure Configuration:

Dual-in-line

# Output Logic Form:

Complementary-metal oxide-semiconductor logic

# **Design Function And Quantity:**

1 phase locked loop

## **Terminal Surface Treatment:**

Solder

## **Product Name:**

Cmos phase lock loop

## Voltage Rating And Type Per Characteristic:

-0.5 volts input and 7.0 volts input

## **Special Features:**

Cmos phase lock loop; 16-lead pl astic dual-in-line package

## **Terminal Type And Quantity:**

16 printed circuit

Shelf Life:

N/a

# Unit Of Measure:

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#### Demilitarization:

Yes - demil/mli

# Fiig:

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