NSN 5962-01-506-6819

Digital Microcircuit - Page 1 of 1



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| Maximum Power Dissipation Rating: |
|---|
| 500.0 milliwatts |
| Operating Tempurature Range: |
| -55.0/+125.0 degrees celsius |
| Storage Tempurature Range: |
| -65.0/+150.0 degrees celsius |
| Features Provided: |
| Electrostatic sensitive and monolithic and bipolar and 3-state output |
| Inclosure Material: |
| Ceramic and glass |
| Inclosure Configuration: |
| Dual-in-line |
| Output Logic Form: |
| Complementary-metal oxide-semiconductor logic |
| Design Function And Quantity: |
| 1 transceiver |
| Terminal Surface Treatment: |
| Solder |
| Product Name: |
| Microcircuit, digital, advanced bipolar cmos, octal latched transceiver with dual enable, three-state outputs, ttl compatible inputs, |
| monolithic silicon |
| Voltage Rating And Type Per Characteristic: |
| -0.5 volts input and 7.0 volts input |
| Special Features: |
| Generic p/n 54abt543a |
| Test Data Document: |
| 96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.). |
| Terminal Type And Quantity: |
| 24 printed circuit |
| Shelf Life: |
| N/a |
| Unit Of Measure: |
| - |
| Demilitarization: |
| No |
| Fiig: |
| A458a0 |
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